

Docket No.: PEK-IN1022



#12/12-6-03 2812#
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By: [Signature]

Date: November 25, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Achim Gratz
Applic. No. : 09/940,001
Filed : August 27, 2001
Title : Vertical Non-Volatile Semiconductor Memory Cell and Method for Manufacturing the Memory Cell
Examiner : Richard A. Booth - Art Unit: 2812

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INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.97(C)(2)

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

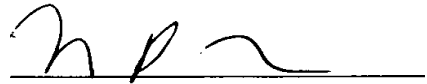
International Publication WO 97/02599 (Hofmann et al.), dated January 23, 1997.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

In accordance with 37 C.F.R. 1.97 (c) (2), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,


For Applicant

Mark P. Weichselbaum
Reg. No. 43,248

Date: November 25, 2002

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